

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Flash Drive/Reader with Serial-Port Controller and Flash-Memory Controller Mastering a Second RAM-Buffer Bus Parallel to a CPU Bus
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Application Number :

Confirmation Number:

First Named Applicant: Horng-Yee Chou

Attorney Docket Number: ML-18

Art Unit:

Examiner:

Search string: (5530828 or 5822251 or 6467015 or 6457092 or 6101566 or 5634073 or 6581124 or 6490639 or 5745732 or 5375089 or 5359715 or 5586248 or 5694556 or 6513094 or 20030018892 or 20030145141).pn

US Patent Documents



Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5530828	1996-06-25	Kaki et al.	B1	711	103
	2	5822251	1998-10-13	Bruce et al.	B1	365	185.13
	3	6467015	2002-10-15	Nolan et al.	B1	711	005
	4	6457092	2002-09-25	Kaki et al.	B1	711	103
	5	6101566	2000-08-08	Woods et al.	B1	710	315
	6	5634073	1997-05-27	Collins et al.	B1	710	005
	7	6581124	2003-06-17	Anand et al.	B1	710	305
	8	6490639	2002-12-03	Delvaux et al.	B1	710	036
	9	5745732	1998-04-28	Cherukuri et al.	B1	711	168
	10	5375089	1994-12-20	Lo	B1	365	189.04
	11	5359715	1994-10-25	Heil et al.	B1	710	312
	12	5586248	1996-12-17	Alexander et al.	B1	714	022
	13	5694556	1997-12-02	Neal et al.	B1	710	310
	14	6513094	2003-01-28	Magro	B1	711	103

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
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	1	20030018892	2003-01-23	Tello	A1	713	164
	2	20030145141	2003-07-31	Chen et al.	A1	710	74

Signature

Examiner Name	Date
	11/17/24